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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Philip L. Hower, et al. Docket No: TI-30010
Serial No: 10/036,323 Conf. No: 3224
Examiner: Thomas L. Dickey Art Unit: 2826
Filed: 12/31/2001
For: N-CHANNEL LDMOS WITH BURIED P-TYPE REGION TO PREVENT PARASITIC BIPOLAR EFFECTS

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-12-03.


Ann Trent

Dear Sir:

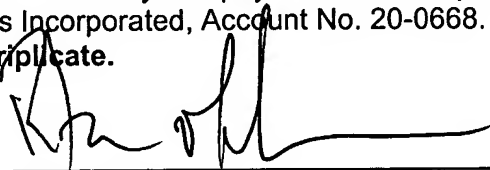
Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed July 30, 2003, finally rejecting Claims 14, 16, and 18.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on _____.
☐ is requested for ____ month.
2. ☒ A timely response to the final rejection has been filed.
3. ☒ Fee \$330.00:
☐ Not required (Fee paid in prior appeal)
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in triplicate.

11/20/2003 MBIZUNES 00000082 200668 10036323
01 FC:1401 330.00 DA

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-4258


Peter K. McLarty
Attorney for Appellants
Reg. No. 44,923